	Application No.	Applicant(s)	VT.5 €
Notice of Allowshills	.10/623,527	KANAMITSU ET AL.	, J
Notice of Allowability	Examiner	Art Unit	1. A. C.
	Thong Q. Le	2818	الم
	2.9	48.4	4
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	or other appropriate communication	pplication. If not included	, in
1: X This communication is responsive to 2/18/2004.			
2. X The allowed claim(s) is/are <u>17-23</u> .			
3 The drawings filed on 22 July 2003 are accepted by the Ex	aminer.		* *
4. ☑ Acknowledgment is made of a claim for foreign priority un a) ☑ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have	been received.		
2. Certified copies of the priority documents have	been received in Application No. 1	<u>10/013,538</u> .	
3. Copies of the certified copies of the priority doc	cuments have been received in this	national stage application f	rom the
International Bureau (PCT Rule 17, 2(a)).		.,	-7.
* Certified copies not received:		* *	
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below: Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (DTG 450).	ENT Of this application.		
INFORMAL PATENT APPLICATION (PTO-152) which give	s reason(s) why the oath or declara	ation is deficient.	E OF
6. CORRECTED DRAWINGS (as "replacement sheets") must	be submitted.		3. " .
(a) including changes required by the Notice of Draftsperso	on's Patent Drawing Review (PTO	-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR 1.8 each sheet. Replacement sheet(s) should be labeled as such in the	34(c)) should be written on the drawing the second of the	ngs in the front (not the back) of
7. DEPOSIT OF and/or INFORMATION about the depos attached Examiner's comment regarding REQUIREMENT F	it of BIOLOGICAL MATERIAL	must be submitted. Nicks A	he 🤃
	*		
	*	. "	
	30		
Attachment(s)		\$ 1	e grande in
1. Notice of References Cited (PTO-892)		atent Application (PTO-152)).
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary	(PTO-413),	
3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date	Paper No./Mail Dat), 7. ☐ Examiner's Amendn	e nent/Comment	
4. Examiner's Comment Regarding Requirement for Deposit	8. 🗌 Examiner's Stateme	ent of Reasons for Allowance	e :
of Biological Material	9. Other	y ,	#.
		Thống Q. Le Primary Examiner	
		Art Unit: 2818	

Art Unit: 2818

DETAILED ACTION

1. Pre-amendment filed on February 18, 2004 has been entered.

2. Claims 17-23 are presented for examination.

Information Disclosure Statement

- This office acknowledges receipt of the following items from the Applicant:
 Information Disclosure Statement (IDS) filed on July 22, 2003.
- 4. Information disclosed and list on PTO 1449 was considered.

Priority

1. Acknowledgment is made of applicant's claim for foreign priority under 35 U.S.C. 119(a)-(d). The certified copy has been filed in parent Application No.10/013,538, filed on 12/13/2001.

Reasons for Allowance

Claims 17-23 are allowed.

The following is an examiner's statement of reasons for allowance:

Claims 17-23 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Sato et al. (U.S. Patent No. 6,026,014), Marquot et al. (U.S. Patent No. 5,978,262) and others, does not teach the claimed invention having a nonvolatile memory device comprises wherein in a program operation, the control circuit controls receiving of data, decoding of data by the first decode circuit, latching of the first signal

Application/Control Number: 10/623,527

Art Unit: 2818

Page 3

in a first state at a first the latch circuit coupled to a first memory cell to be subjected to a first level programming, a program voltage to a word line coupled to the first memory cell after latching at the first latch circuit, latching of the first signal in the first state at a second the latch circuit coupled to a second memory cell to be subjected to a second level programming, and the program voltage to a word line coupled to the second memory cell after latching at the second latch circuit.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thong Q. Le whose telephone number is 571-272-1783. The examiner can normally be reached on 8:00am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thong Q. Le
Primary Examiner

THONG LE